

## N-CHANNEL ENHANCEMENT MOS FET

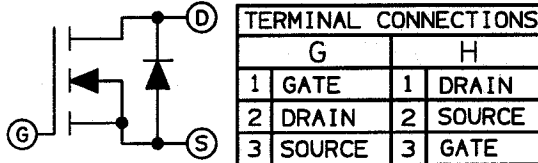
1000V, 5.0A, 2.4 Ω

SDF5N100 JAA  
SDF5N100 JAB

### FEATURES

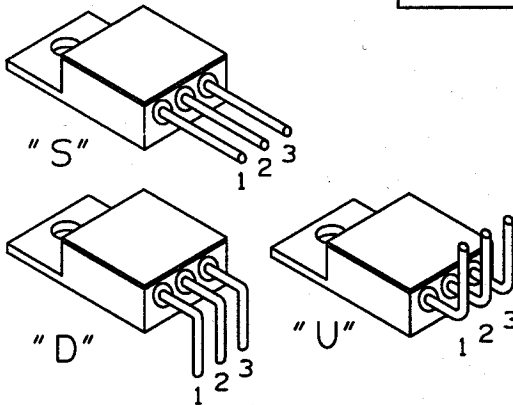
- RUGGED PACKAGE
- HI-REL CONSTRUCTION
- CERAMIC EYELETS
- LEAD BENDING OPTIONS
- COPPER CORED 52 ALLOY PINS
- LOW IR LOSSES
- LOW THERMAL RESISTANCE
- OPTIONAL MIL-S-19500 SCREENING

### SCHEMATIC



### STANDARD BEND CONFIGURATIONS

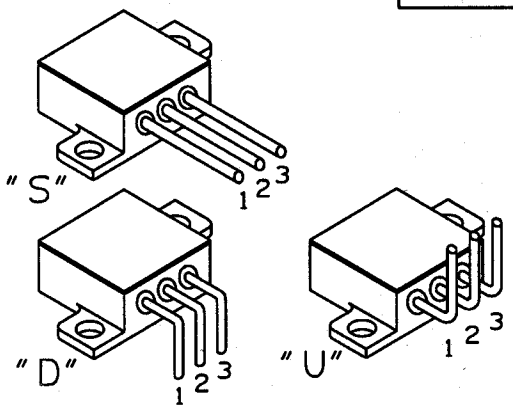
JAA



(CUSTOM BEND OPTIONS AVAILABLE)

### STANDARD BEND CONFIGURATIONS

JAB



(CUSTOM BEND OPTIONS AVAILABLE)

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL		UNITS
Drain-source Volt.(1)	VDSS	1000	Vdc
Drain-Gate Voltage (Rgs=1.0MΩ) (1)	VDGR	1000	Vdc
Gate-Source Voltage Continuous	VGS	±20	Vdc
Drain Current Continuous (Tc = 25°C)	ID	5.0	Adc
Drain Current Pulsed(3)	IDM	20	A
Total Power Dissipation	PD	150	W
Power Dissipation Derating > 25°C		1.2	W/°C
Operating & Storage Temp.	TJ/Tsig	-55 TO +150	°C
Thermal Resistance	RthJc	0.8	°C/W
Max.Lead temperature	TL	300	°C

### ELECTRICAL CHARACTERISTICS Tc = 25°C (UNLESS OTHERWISE SPECIFIED)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-source Breakdown Volt.	V(BR)DSS	VGS=0V ID=250 μA	1000	-	-	V
Gate Threshold Voltage	VGS(TH)	VDS=VGS ID=250 μA	2.0	-	4.5	V
Gate Source Leakage	IGSS	VGS=±20 V	-	-	100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=MAX.RATING VGS=0	-	-	250	μA
		VDS=0.8 MAX.RATING VGS=0 TJ=125°C	-	-	1000	μA
Static Drain-Source On-State Resistance(1)	RDS(ON)	VGS=10 V ID=2.5A	-	-	2.4	Ω
Forward Trans-Conductance (2)	gfs	VDS ≥ 15 V IDS=2.5A	5.2	-	-	S(O)
Input Capacitance	CISS		-	2700	-	pF
Output Capacitance	COSS	VGS=0V VDS=25 V f=1.0 MHz	-	200	-	pF
Reverse Transfer Capacitance	CRSS		-	60	-	pF
Turn-On Delay	td(on)	VDD=500V Zo=5 n	-	-	40	ns
Rise Time	tr	ID=2.5A	-	-	45	ns
Turn-Off Delay	td(off)	(MOSFET switching times are essentially independent of operating temp.)	-	-	100	ns
Fall Time	tf		-	-	65	ns
Total Gate Charge (Gate-Source Plus Gate-Drain)	Qg	VGS=10V, ID=5.0A	-	-	120	nC
Gate-Source Charge	Qgs	VDS=0.8 MAX.RATING (Gate charge is essentially independent of the operating temperature)	-	-	15	nC
Gate-Drain ("Miller") Charge	Qgd		-	-	50	nC

### SOURCE-DRAIN DIODE RATINGS & CHARACT. Tc = 25°C (UNLESS OTHERWISE SPECIFIED)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Continuous Source Current (Body Diode)	IS	Modified MOSFET symbol showing the integral reverse P-N junction rectifier (See schematic)	-	-	5.0	A
Pulse Source Current (Body Diode) (1)	ISM		-	-	20	A
Diode Forward Voltage (2)	VSD	IF=5.0A VGS=0V Tc=+25°C	-	-	1.5	V
Reverse Recovery Time	trr	Tc=+25° C IF=5.0A di/dt=100A/μS	-	800	-	ns

(1) TJ = 25°C to 150°C.

(2) Pulse test: Pulse Width < 300μS, Duty Cycle < 2%.

(3) Repetitive Rating: Pulse Width limited By Max.Junction Temperature.