

N-CHANNEL ENHANCEMENT MOS FET

ABSOLUTE MAXIMUM RATINGS			
PARAMETER	SYMBOL		UNITS
Drain-source Volt.(1)	VDSS	200	Vdc
Drain-Gate Voltage (RGS=1.0Mn) (1)	VDGR	200	Vdc
Gate-Source Voltage Continuous	VGS	±20	Vdc
Drain Current Continuous (Tc = 25°C)	ID	5.0	Adc
Drain Current Pulsed(3)	IDM	20	A
Total Power Dissipation	PD	50	W
Power Dissipation Derating > 25°C		0.4	W/°C
Operating & Storage Temp.	TJ/Tsig	-55 TO +150	°C
Thermal Resistance	RthJc	2.5	°C/W
Max. Lead temperature	TL	300	°C

200V, 5.0A, 0.8 Ω

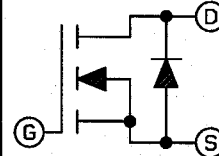
SDF220 JAA
SDF220 JAB
SDF220 JDA

FEATURES

- RUGGED PACKAGE
- HI-REL CONSTRUCTION
- CERAMIC EYELETS: JAA, JAB
- LEAD BENDING OPTIONS
- COPPER CORED 52 ALLOY PINS
- LOW IR LOSSES
- LOW THERMAL RESISTANCE
- OPTIONAL MIL-S-19500 SCREENING

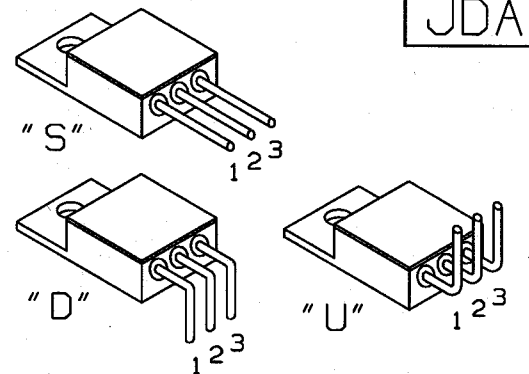
ELECTRICAL CHARACTERISTICS Tc = 25°C (UNLESS OTHERWISE SPECIFIED)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-source Breakdown Volt.	V(BR)DSS	VGS=0V ID=250 μA	200	-	-	V
Gate Threshold Voltage	VGS(TH)	VDS=VGS ID=250 μA	2.0	-	4.0	V
Gate Source Leakage	IGSS	VGS=±20 V	-	-	100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=MAX.RATING VGS=0	-	-	250	μA
		VDS=0.8 MAX.RATING VGS=0 TJ=125°C	-	-	1000	μA
Static Drain-Source On-State Resistance(1)	RDS(ON)	VGS=10 V ID=2.5A	-	-	0.8	Ω
Forward Trans-Conductance (2)	gfs	VDS ≥ 50 V IDS=2.5A	1.3	-	-	S(U)
Input Capacitance	CISS	VGS=0V VDS=25 V f=1.0 MHz	-	450	-	pF
Output Capacitance	COSS		-	150	-	pF
Reverse Transfer Capacitance	CRSS		-	40	-	pF
Turn-On Delay	td(on)	VDD=100V RGS=50 n ID=2.5A RD=50 n	-	-	40	ns
Rise Time	tr	(MOSFET switching times are essentially independent of operating temp.)	-	-	60	ns
Turn-Off Delay	td(off)		-	-	100	ns
Fall Time	tf		-	-	60	ns
Total Gate Charge (Gate-Source Plus Gate-Drain)	Qg	VGS=10V, ID=6.0A VDS=0.8 MAX.RATING (Gate charge is essentially independent of the operating temperature)	-	-	15	nC
Gate-Source Charge	Qgs		-	5.0	-	nC
Gate-Drain ("Miller") Charge	Qgd		-	6.0	-	nC

SCHEMATIC



TERMINAL CONNECTIONS				
	G		H	
1	GATE	1	DRAIN	
2	DRAIN	2	SOURCE	
3	SOURCE	3	GATE	

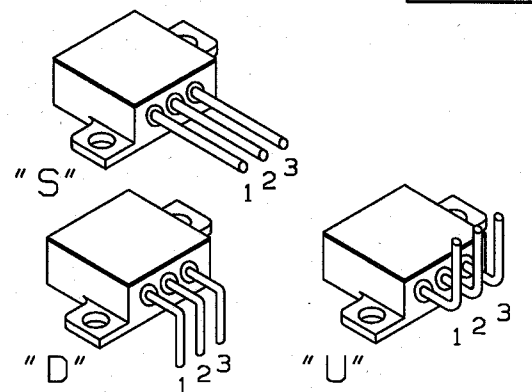
STANDARD BEND CONFIGURATIONS



JAA
JDA

(CUSTOM BEND OPTIONS AVAILABLE)

STANDARD BEND CONFIGURATIONS



JAB

(CUSTOM BEND OPTIONS AVAILABLE)

SOURCE-DRAIN DIODE RATINGS & CHARACT. Tc = 25°C (UNLESS OTHERWISE SPECIFIED)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Continuous Source Current (Body Diode)	IS	Modified MOSFET symbol showing the integral reverse P-N junction rectifier (See schematic)	-	-	5.0	A
Pulse Source Current (Body Diode) (1)	ISM		-	-	4.0	A
Diode Forward Voltage (2)	VSD	IF=5.0A VGS=0V Tc=+25°C	-	-	2.0	V
Reverse Recovery Time	trr	Tc=+25°C	-	350	-	ns
Reverse Recovery Charge	Qrr	IF=5.0A di/dt=100A/μS	-	2.3	-	μC

(1) TJ = 25°C to 150°C.
(2) Pulse test: Pulse Width < 300μS, Duty Cycle < 2%.
(3) Repetitive Rating: Pulse Width limited By Max. Junction Temperature.