

ABSOLUTE MAXIMUM RATINGS			
PARAMETER	SYMBOL		UNITS
Drain-source Volt. (1)	VDSS	600	Vdc
Drain-Gate Voltage (R _{GS} =1.0M Ω) (1)	VDGR	600	Vdc
Gate-Source Voltage Continuous	VGS	± 20	Vdc
Drain Current Continuous (T _c = 25°C)	ID	20	Adc
Drain Current Pulsed(3)	IDM	80	A
Total Power Dissipation	PD	300	W
Power Dissipation Derating > 25°C		2.4	W/°C
Operating & Storage Temp.	T _J /T _{stg}	-55 TO +150	°C
Thermal Resistance	R _{thJc}	0.42	°C/W
Max. Lead temperature	TL	300	°C

600V, 20A, 0.35 Ω

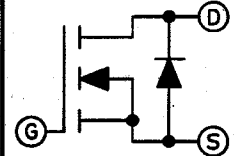
SDF20N60 JEA
 SDF20N60 JEB
 SDF20N60 JEC
 SDF20N60 JED

FEATURES

- RUGGED PACKAGE
- HI-REL CONSTRUCTION
- CERAMIC EYELETS
- LEAD BENDING OPTIONS
- COPPER CORED 52 ALLOY PINS
- LOW IR LOSSES
- LOW THERMAL RESISTANCE
- OPTIONAL MIL-S-19500 SCREENING

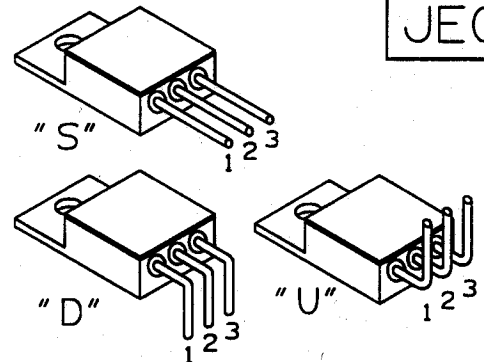
ELECTRICAL CHARACTERISTICS T _c = 25°C (UNLESS OTHERWISE SPECIFIED)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-source Breakdown Volt.	V(BR)DSS	VGS=0V ID=250 μ A	600	-	-	V
Gate Threshold Voltage	VGS(TH)	VDS=VGS ID=250 μ A	2.0	-	4.5	V
Gate Source Leakage	IGSS	VGS= ± 20 V	-	-	100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=MAX. RATING VGS=0	-	-	250	μ A
		VDS=0.8 MAX. RATING VGS=0 T _J =125°C	-	-	1000	μ A
Static Drain-Source On-State Resistance(1)	RDS(ON)	VGS=10 V ID=10A	-	-	.35	Ω
Forward Trans-Conductance (2)	g _{fs}	VDS \geq 50 V IDS=10A	9.0	-	-	S(U)
Input Capacitance	CISS	VGS=0V VDS=25 V f=1.0 MHz	-	4500	-	pF
Output Capacitance	COSS		-	550	-	pF
Reverse Transfer Capacitance	CRSS		-	160	-	pF
Turn-On Delay	t _{d(on)}	VDD=300V Z _o =50 Ω ID=10A	-	-	100	ns
Rise Time	t _r	(MOSFET switching times are essentially independent of operating temp.)	-	-	110	ns
Turn-Off Delay	t _{d(off)}		-	-	220	ns
Fall Time	t _f		-	-	105	ns
Total Gate Charge (Gate-Source Plus Gate-Drain)	Q _g	VGS=10V, ID=20A VDS=0.8 MAX. RATING (Gate charge is essentially independent of the operating temperature)	-	165	-	nC
Gate-Source Charge	Q _{gs}		-	65	-	nC
Gate-Drain Charge ("Miller")	Q _{gd}		-	100	-	nC

SCHEMATIC



TERMINAL CONNECTIONS	
G	H
1 GATE	1 DRAIN
2 DRAIN	2 SOURCE
3 SOURCE	3 GATE

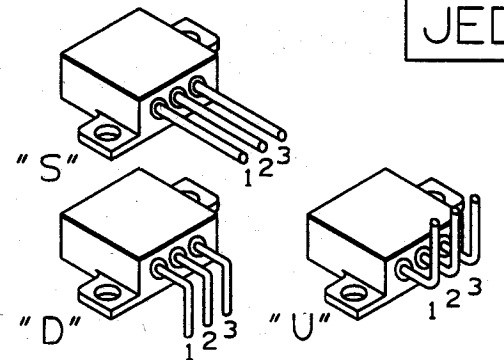
STANDARD BEND CONFIGURATIONS



JEA
JEC

(CUSTOM BEND OPTIONS AVAILABLE)

STANDARD BEND CONFIGURATIONS



JEB
JED

(CUSTOM BEND OPTIONS AVAILABLE)

SOURCE-DRAIN DIODE RATINGS & CHARACT. T _c = 25°C (UNLESS OTHERWISE SPECIFIED)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Continuous Source Current (Body Diode)	IS	Modified MOSFET symbol showing the integral reverse P-N junction rectifier (See schematic)	-	-	20	A
Pulse Source Current (Body Diode) (1)	ISM		-	-	80	A
Diode Forward Voltage (2)	VSD	IF=20A VGS=0V T _c =25°C	-	-	1.5	V
Reverse Recovery Time	t _{rr}	T _c =25°C IF=20A	-	500	-	ns
Reverse Recovery Charge	Q _{rr}	di/dt=100A/ μ S	-	8.0	-	μ C

(1) T_J = 25°C to 150°C.
 (2) Pulse test: Pulse Width < 300 μ S, Duty Cycle < 2%.
 (3) Repetitive Rating: Pulse Width limited By Max. Junction Temperature.